



1/8

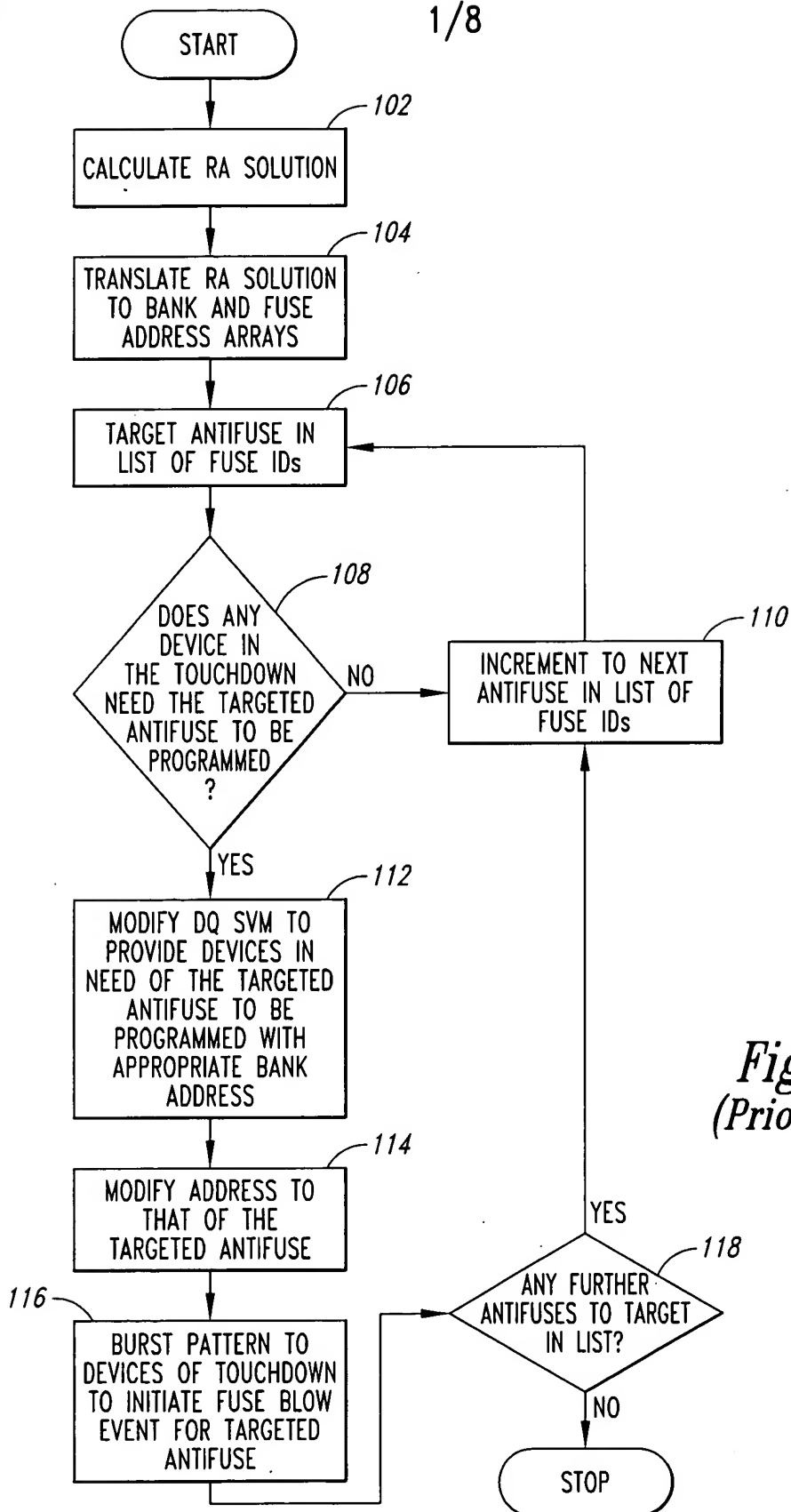


Fig. 1
(Prior Art)

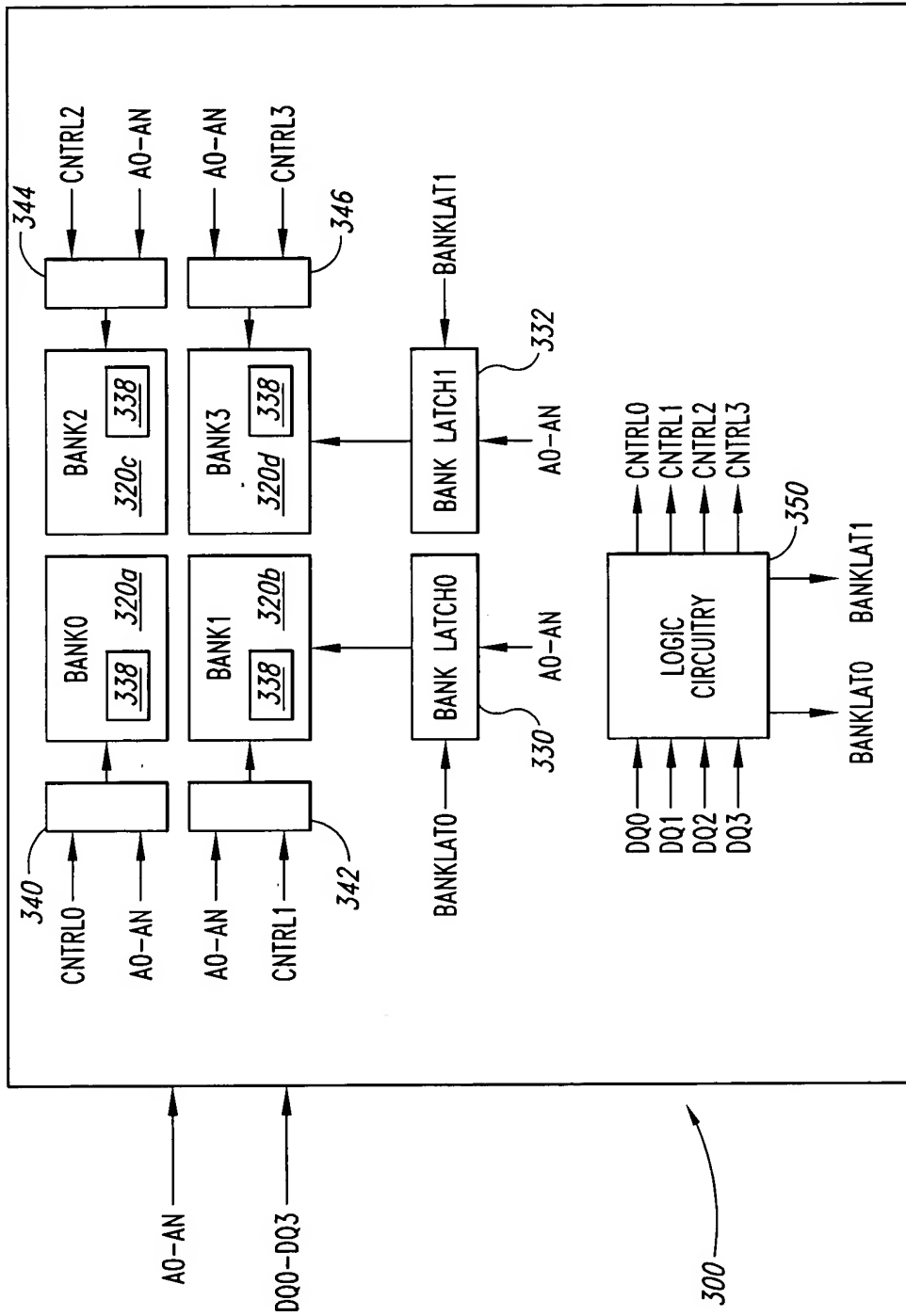


Fig. 2

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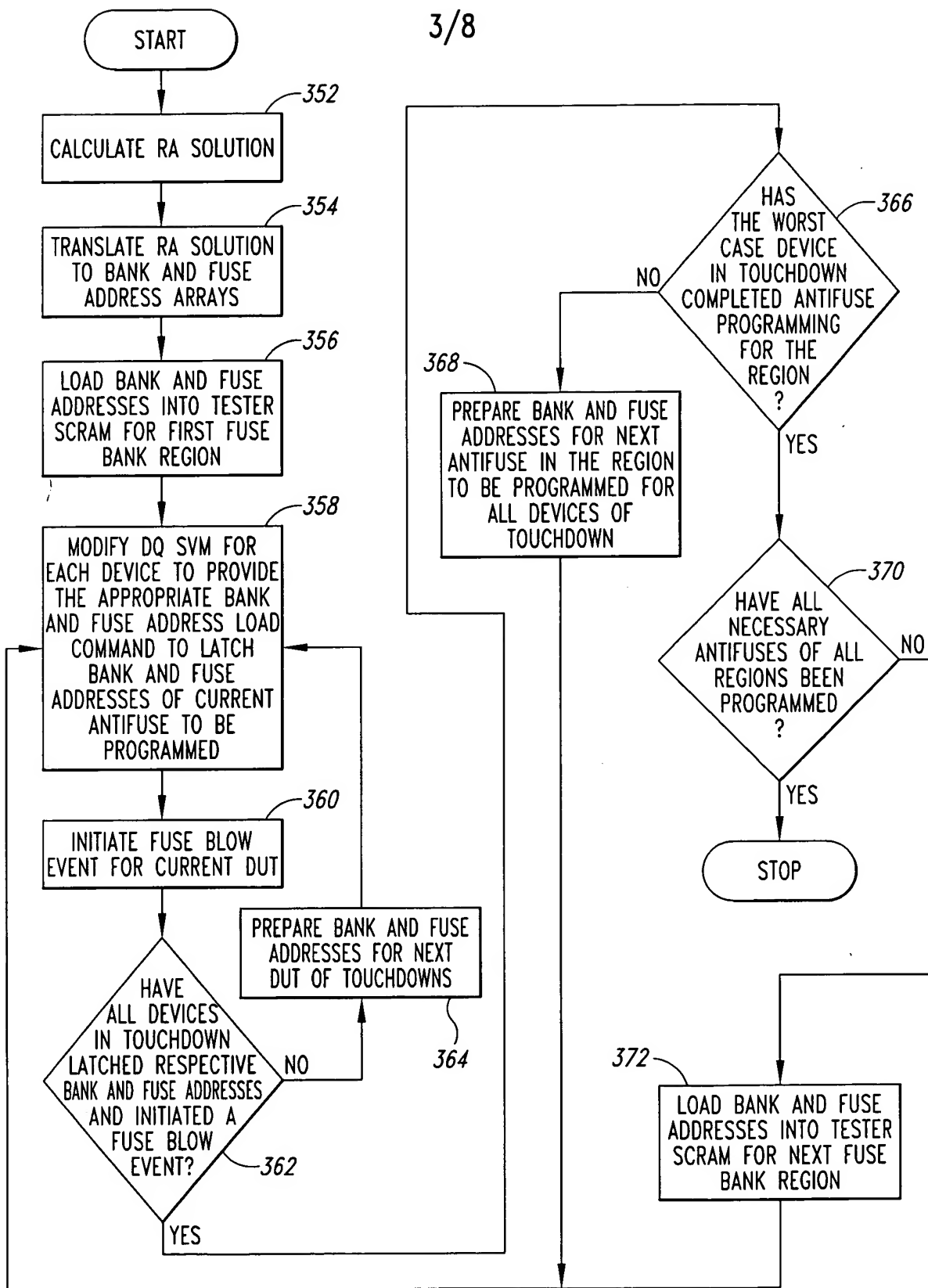


Fig. 3

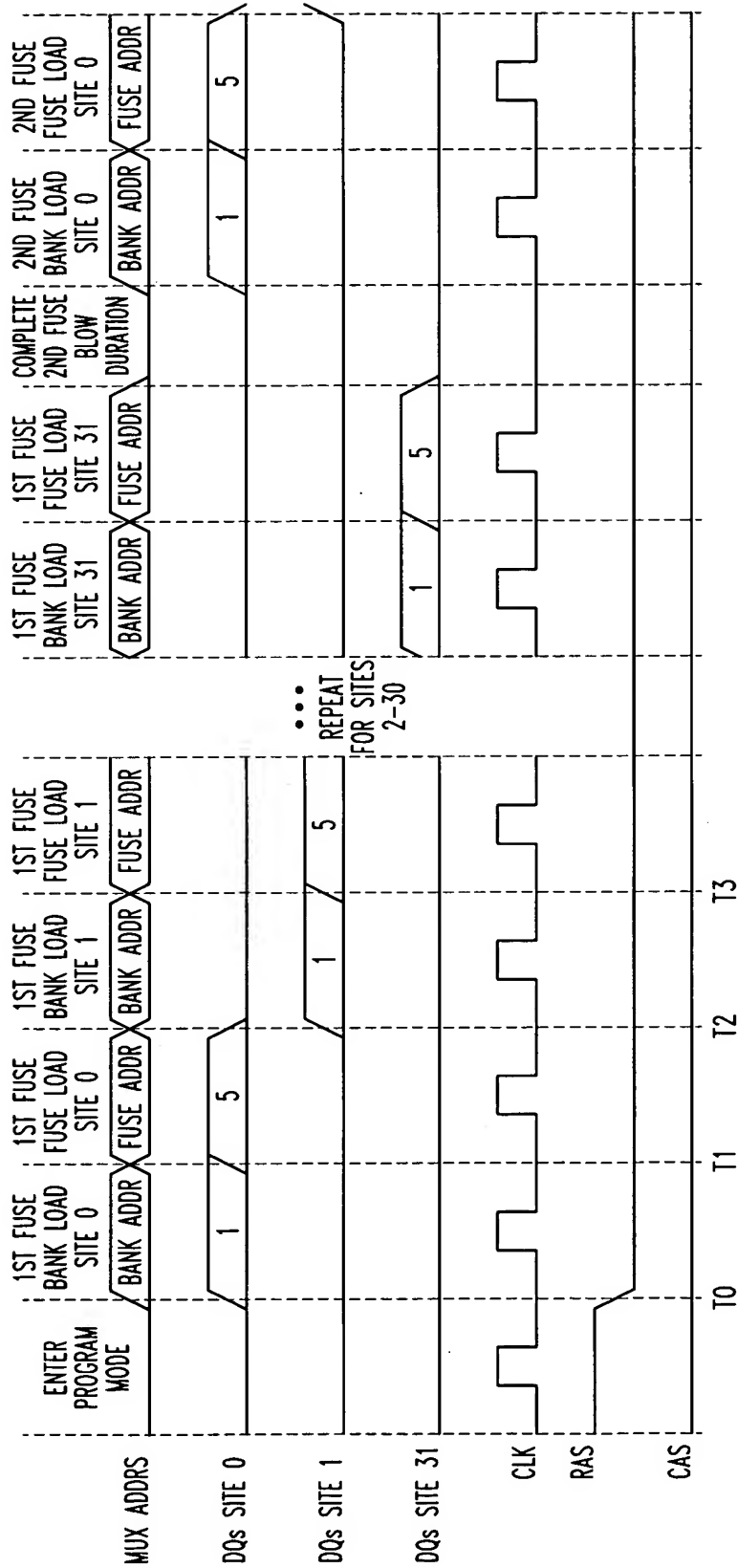


Fig. 4



500

SCRAM RAM		DQ SVM DATA			
		DUT 0 0123	DUT 1 0123	DUT 31 0123	DUT 63 0123
502	BANK	1000	0000	0000	0000
504	FUSE	1010	0000	0000	0000
502	BANK	0000	1000	0000	0000
504	FUSE	0000	1010	0000	0000
502	BANK	0000	0000	1000	0000
504	FUSE	0000	0000	1010	0000
502	BANK	0000	0000	0000	1000
504	FUSE	0000	0000	0000	1010

Fig. 5

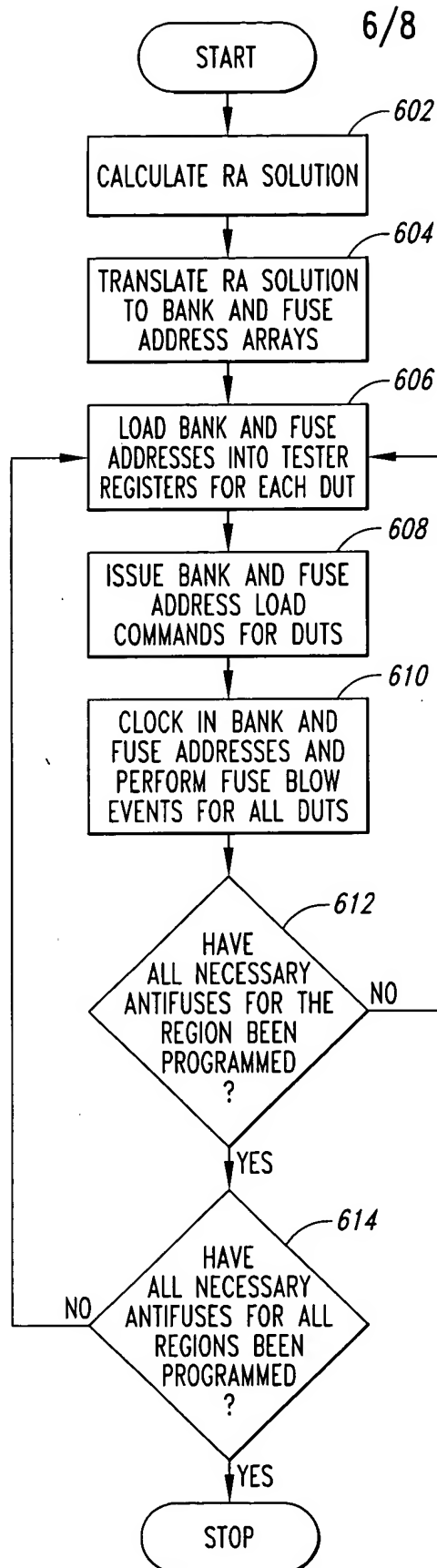


Fig. 6

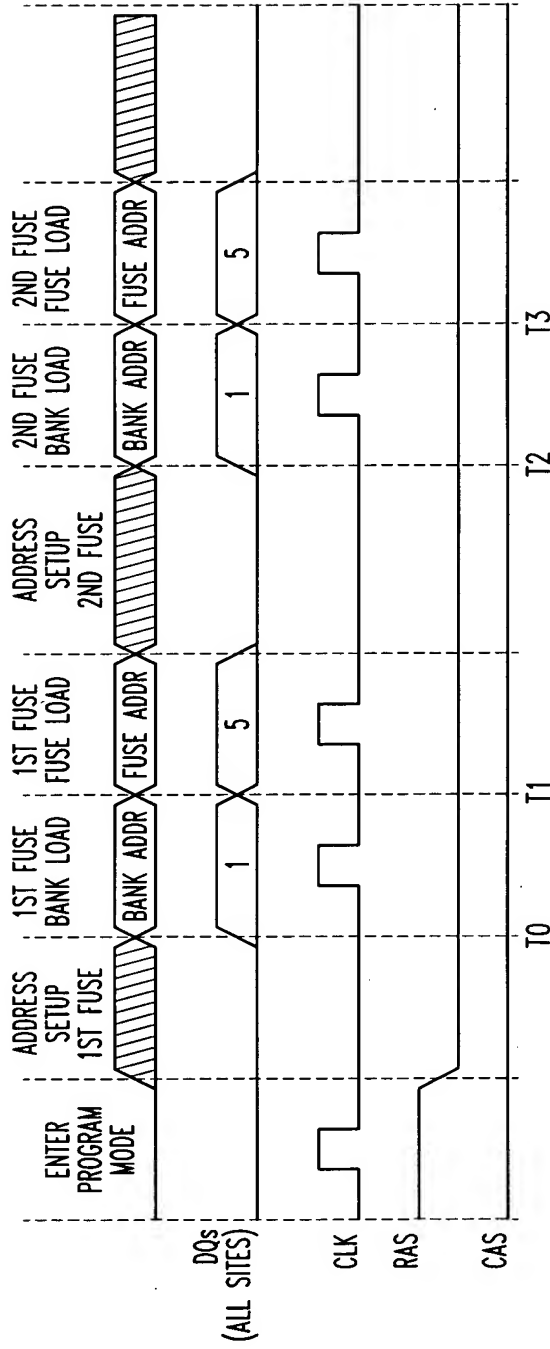


Fig. 7

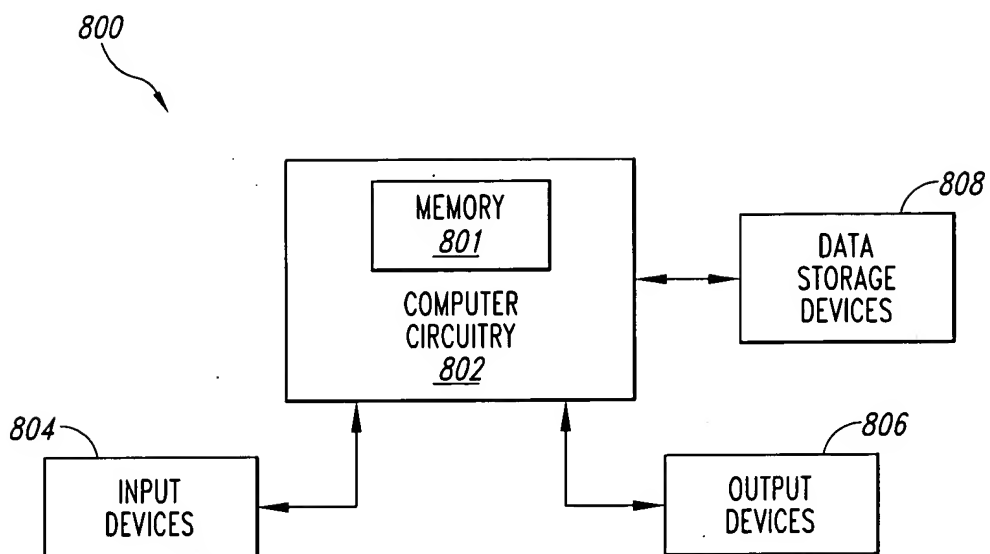


Fig. 8